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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/779,783	02/18/2004	Hiroyuki Hosaka	118474	1634
25944	7590	07/26/2007	EXAMINER	
OLIFF & BERRIDGE, PLC			SHANKAR, VIJAY	
P.O. BOX 19928			ART UNIT	PAPER NUMBER
ALEXANDRIA, VA 22320			2629	
			MAIL DATE	DELIVERY MODE
			07/26/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)
	10/779,783	HOSAKA ET AL.
	Examiner	Art Unit
	VIJAY SHANKAR	2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 27 April 2007.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-22 is/are pending in the application.
 4a) Of the above claim(s) 2-6,8-10,12-14,16-18 is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1,7,11,15 and 19-22 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO/SB/08)
 Paper No(s)/Mail Date _____

4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
 5) Notice of Informal Patent Application
 6) Other: _____.

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Election/Restrictions

2. Applicant's election with traverse of Species I with Claims 1,7,11,15,19-22 in the reply filed on 4/27/07 is acknowledged. The traversal is on the ground(s) that the subject matter of all species is sufficiently related . Thus, it is respectfully submitted that the search and examination of the entire application could be made without serious burden.

This is not found persuasive because all ten species were claimed in this application. All ten embodiments of species are different from each other and it would require different search .

The requirement is still deemed proper and is therefore made FINAL.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1,7,11,15,19-22 are rejected under 35 U.S.C. 102(e) as being anticipated by Matsueda (US Pub. 2002/0145602 A1).

Regarding Claims 1 and 7, Matsueda teaches a drive circuit or method for driving a display device including an active matrix substrate provided with a plurality of pixel electrodes arranged in a matrix, an opposing substrate provided with a transparent opposing electrode, and a liquid crystal layer held between the active matrix substrate and the opposing substrate, (Figures 1-18; Paragraph 0069-0116) the drive circuit comprising: a first signal supplying unit that supplies an image signal to the pixel electrodes (Paragraph 0091, 0099, 0101); a first detector that detects, on the basis of the image signal per unit time (Paragraph 0108-0111), a first gray level characterizing the brightness of an image (Figures 4,5; Paragraph 0074-0083, 0125); a variation-signal setting unit that sets a variation signal on the basis of the first gray level (Paragraph 0108-0112); and a second signal supplying unit that supplies the variation signal to the opposing electrode, the liquid crystal layer being driven by an effective voltage signal generated by modulating the image signal using the variation signal (Figures 17-18; Paragraph 0091-0101, 0112-0116), and the variation-signal setting unit setting the variation signal so that the gray level of the effective voltage signal becomes greater than the gray level of the image signal in accordance with an increase in the first gray level. (Figs.4,5, Paragraph 0074-0083, 97-101,0125).

Regarding Claim 11, Matsueda teaches a display device, comprising: an active matrix substrate provided with a plurality of pixel electrodes arranged in a matrix; an opposing substrate provided with a transparent opposing electrode; a liquid crystal layer held between the active matrix substrate and the opposing substrate (Figs.1-2,16, Paragraph 0076-0077, 0109).

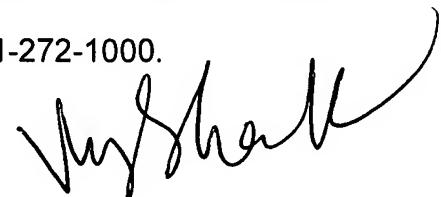
Regarding Claim 15, Matsueda teaches a projection display device, comprising: a light source; a light modulator including an active matrix substrate provided with a plurality of pixel electrodes arranged in a matrix, an opposing substrate provided with a transparent opposing electrode, and a liquid crystal layer held between the active matrix substrate and the opposing substrate; the light modulator; and a projection optical system that projects light emitted from the light modulator. (Figs.1-2,16, Paragraph 0076-0077, 0109).

Regarding Claims 19-22, Matsueda teaches a drive circuit for driving a display device wherein the variation signal setting unit including a setting table that defines a relationship between the first gray level and the variation signal. (Figs.4,5, Paragraph 0074-0083, 0108-0112, 0125).

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



VIJAY SHANKAR
Primary Examiner
Art Unit 2629

VS